



FEATURES

- 3.3V Operation
- Stocking Standard
- HCMOS Output
- Enable/Disable
- Tape and Reel (2,000 pcs. STD)



• PART NUMBER SELECTION [Learn More](#) - Internet Required

Part Number	Model Number	Frequency Stability	Operating Temperature (°C)	Frequency Range (MHz)
424-Frequency-xxxxx	VCS25AXT	±50PPM ³	-10 ~ +70	1.000 ~ 80.000 ¹

• ELECTRICAL CHARACTERISTICS

PARAMETERS	MAX (unless otherwise noted)
Frequency Range (Fo)	1.000 ~ 80.000 ¹ MHz
Temperature Range	
Operating (TOPR)	-10°C ~ +70°C ³
Storage (TSTG)	-40°C ~ +85°C
Frequency Stability	±50PPM ³
Pullability	±100PPM ³
Supply Voltage (VDD)	3.3V ± 10%
Control Voltage (Vc)	1.65V ± 1.5V
Input Current (IDD)	
1.000 ~ 30.000 MHz	15mA
30.000+ ~ 45.000 MHz	25mA
45.000+ ~ 80.000 MHz	50mA
Output Symmetry (50% VDD)	40% ~ 60%
Rise Time (10% ~ 90% VDD) (TR)	5nS
Fall Time (90% ~ 10% VDD) (TF)	5nS
Output Voltage (VOL)	10% VDD
(VOH)	90% VDD Min
Output Current (IOL)	4.0mA Min
(IOH)	-1.0mA Min
Output Load (HCMOS)	15pF
Start-up Time (TS)	10mS
Enable/Disable Time ²	150nS
Frequency Linearity	±10%
Modulation Bandwidth	20 kHz

¹ Custom specifications from 45.000 to 80.000 MHz available on an individual inquiry basis.

² An internal pullup resistor from pin 2 to pin 6 allows active output if pin 2 is left open.

³ Other temperature ranges, stabilities, and pullabilities available.

Note: A 0.01µF bypass capacitor should be placed between VDD (Pin 6) and GND (Pin 3) to minimize power supply line noise.

All specifications subject to change without notice. Rev. 8/25/05

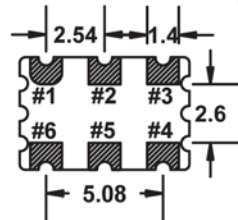
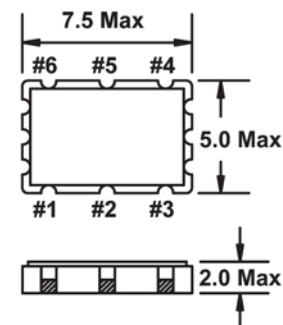
• DISTRIBUTOR STOCKING ITEMS

Model Number	Frequency (MHz)
VCS25AXT-0496	4.096
VCS25AXT-081	8.192
VCS25AXT-128	12.288
VCS25AXT-163	16.384
VCS25AXT-270	27.000
VCS25AXT-353	35.328
VCS25AXT-447	44.736

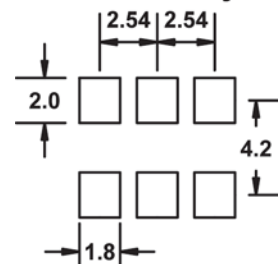


Learn more about: Part Marking Identification Tape and Reel Specification

Internet required



Recommended Solder Pad Layout



Pin Connections

#1 V _{Control}	#4 Output
#2 E/D	#5 N.C.
#3 GND	#6 V _{DD}

All dimensions are in millimeters.

• ENABLE / DISABLE FUNCTION

INH (Pin 2)	OUTPUT (Pin 4)
OPEN ²	ACTIVE
'1' Level V _{IH} ≥ 70% V _{DD}	ACTIVE
'0' Level V _{IL} ≤ 30% V _{DD}	High Z